Title: METHOD OF PACKAGING AT A WAFER LEVEL

IN THE CLAIMS

Please amend the claims as follows.

(Currently Amended) A method of packaging comprising: 1.

applying an adhesive to a first side of a finished wafer, the first side of the finished wafer having at least one die, the adhesive being one or more of an elastomer applied in fluid form, a thermoplastic material, or a pressure-sensitive film; and

forming an array of conductive elements within the adhesive to a level to allow the adhesive to contact a support to attach the at least one die to the support, the array of conductive elements electrically coupled to an array of connection pads on the at least one die.

(Original) The method of claim 1, wherein forming an array of conductive elements 2. includes:

creating openings in the adhesive, the openings aligned with the array of connection pads; and

substantially filling the openings with an electrically conductive material.

- (Original) The method of claim 1, wherein the method is performed in the order 3. presented.
- (Currently Amended) A method of packaging comprising: 4.

applying an adhesive to a first side of a finished wafer, the first side of the finished wafer having at least one die, the adhesive being an elastomer applied in fluid form;

processing the adhesive to create an array of openings therein, the array of openings providing access to an array of connection pads on the at least one die; and

substantially filling the array of openings with an electrically conductive material to a level to allow the adhesive to contact a support to attach the at least one die to the support.

(Original) The method of claim 4, wherein the method further includes applying a 5. protective coating to a second side of the wafer.

- (Original) The method of claim 4, wherein the method further includes singulating the at 6. least one die from the wafer wherein the at least one die with the adhesive and electrically conductive material form an individual flip chip package.
- (Original) The method of claim 6, wherein the method further includes surface mounting 7. the flip chip package to a receiving support.
- (Original) The method of claim 4, wherein the method further includes curing the 8. adhesive.
- (Original) The method of claim 4, wherein the method is performed in the order 9. presented.
- 10.-26. (Cancelled)
- (Previously Presented) A method of packaging comprising: 27. forming an array of conductive elements within an adhesive layer; and applying the adhesive layer to a first side of a finished wafer, the first side of the finished wafer having one or more dice, after forming the array of conductive elements to couple the array of conductive elements electrically to an array of connection pads on a first die of the one or more dice.
- 28. (Original) The method of claim 27, wherein forming an array of conductive elements within an adhesive layer includes forming openings in the adhesive layer.
- (Original) The method of claim 28, wherein forming openings in the adhesive layer 29. includes forming openings by laser cutting, chemical etching, or die cutting.
- (Original) The method of claim 27, wherein forming an array of conductive elements 30. includes forming an array of solder columns.

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- 31. (Original) The method of claim 27, wherein forming an array of conductive elements includes forming an array of solder balls.
- 32. (Original) The method of claim 27, wherein applying the adhesive layer includes applying the adhesive layer configured as a film.
- 33. (Original) The method of claim 27, wherein the method further includes singulating the first die from the finished wafer and forming an individual flip chip package.
- 34.-58. (Cancelled)
- 59. (New) A method of packaging comprising:

applying an adhesive to a first side of a finished wafer, the first side of the finished wafer having at least one die, the adhesive being an elastomer applied in fluid form;

processing the adhesive to create an array of openings therein, the array of openings providing access to an array of connection pads on the at least one die; and

substantially filling the array of openings with an electrically conductive paste material to a level to allow the adhesive to contact a support to attach the at least one die to the support.

- 60. (New) The method of claim 59, wherein the method includes applying a dispensing apparatus to place the paste in the openings.
- 61. (New) The method of claim 59, wherein the method includes applying stencil/screen techniques to place the paste in the openings.
- 62. (New) The method of claim 59, wherein the adhesive is cured prior to filling the array of openings with an electrically conductive paste material.
- 63. (New) The method of claim 59, wherein the method includes singulating the finished wafer to provide the at least one die as an individual die.

AMENDMENT AND RESPONSE UNDER 37 CFR \S 1.116

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(New) The method of claim 63, wherein the method includes coupling the at least one 64. die as an individual die to a motherboard.